

Amendments to the Specification

On page 1, after the title “OPTIMIZATION OF N-BASE TYPED ARITHMETIC EXPRESSIONS” please insert the following:

-- CROSS-REFERENCE TO RELATED APPLICATION

This application is a continuation of U.S. Patent Application Serial No. 10/002,437, entitled “Optimization Of N-Base Typed Arithmetic Expressions,” by Zhiquan Chen and Judy Schwabe, filed on November 1, 2001, which is a continuation of application no. 09/439,113, now U.S. Patent 6,363,523, entitled “Optimization Of N-Base Type Arithmetic Expressions,” by Zhiquan Chen and Judith Schwabe filed on November 12, 1999.

This application is related to the following:

U.S. Patent Application Serial No. 09/243,101, entitled “Object-Oriented Instruction Set For Resource Constrained Devices”, by Joshua Susser and Judith Schwabe, filed on February 2, 1999, commonly assigned herewith.

U.S. Patent Application Serial No. 10/664,216, entitled “Virtual Machine With Securely Distributed Bytecode Verification”, by Moshe Levy and Judith Schwabe, filed on September 16, 2003, commonly assigned herewith, which is a continuation of application no. 10/283,305, entitled “Virtual Machine With Securely Distributed Bytecode Verification”, by Moshe Levy and Judith Schwabe, filed on October 30, 2002, which is a continuation of application no. 09/547,225, now U.S. Patent 6,546,454, entitled “Virtual Machine With Securely Distributed Bytecode Verification”, by Moshe Levy and Judith Schwabe, filed on April 11, 2000, which is a continuation of application no. 08/839,621, now U.S. Patent 6,092,147, entitled “Virtual Machine With Securely Distributed Bytecode Verification”, by Moshe Levy and Judith Schwabe, filed on April 15, 1997.--